

ASSIGNMENT OF PATENT RIGHTS

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- (i) damages,
- (ii) injunctive relief and
- (iii) other remedies of any kind

for past, current and future infringement; and

(g) all rights to collect royalties and other payments under or on account of any of the foregoing.

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-001	Switched-Mode Power Amplifier Integrally Performing Power Combining	Issued	6,603,352	U.S.A.	Wight, James	12/3/2001
ICE-001PC	Switched-Mode Power Amplifier Integrally Performing Power Combining	Nationalized	CA02/01847	PCT	Wight, James	12/3/2002
ICE-001JP	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2003-550250	Japan	Wight, James	12/3/2002
ICE-001KR	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	7008505/2004	Korea	Wight, James	06/03/2004
ICE-001CN	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2824126.6	China	Wight, James	12/3/2001
ICE-001AU**	Switched-Mode Power Amplifier Integrally Performing Power Combining	Lapsed	2002351903	Australia	Wight, James	12/3/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-001CP	Switched-Mode Power Amplifier Integrally Performing Power Combining (CIP)	Issued	6,937,096	U.S.A.	Wight, James	6/30/2003
ICE-002PR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Expired	60/307/889	U.S.A.	Wight, James	7/27/01
ICE-002	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	10/068,120	U.S.A.	Wight, James	2/6/2002
ICE-002PC	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Nationalized	CA02/01150	PCT	Wight, James	7/26/2002
ICE-002CA	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Abandoned	2455111	Canada	Wight, James	7/26/2002
ICE-002CN	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Pending	2818192.1	China	Wight, James	7/26/2002
ICE-002EP	Reception Diversity Combiner	Pending	2748525.9	EPO	Wight, James	7/26/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
	with Selectable Inversion and Variable Gain					
ICE-002JP	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	2003-518082	Japan	Wight, James	7/26/2002
ICE-002KR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Pending	7001206/2004	Korea	Wight, James	01/27/2004
ICE-002NO	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned but revivable	20040269	Norway	Wight, James	7/26/2002
ICE-003	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Allowed	10/094,826	U.S.A.	Wight, James	3/11/2002
ICE-003PC	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Expired	CA02/01498	PCT	Wight, James	10/4/2002
ICE-003AU**	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Lapsed	2002328744	Australia	Wight, James	10/4/2002
ICE-004	Up/Down Conversion Circuitry for Radio Transceiver	Pending	10/154,282	U.S.A.	Birkett, Alexander	5/22/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-004PC	Up/Down Conversion Circuitry for Radio Transceiver	Expired	CA02/01497	PCT	Birkett, Alexander	10/4/2002
ICE-004AU**	Up/Down Conversion Circuitry for Radio Transceiver	Lapsed	2002328743	Australia	Birkett, Alexander	10/4/2002
ICE-005	Oscillator Frequency Offsets	Abandoned	10/155,107	U.S.A.	Birkett, Alexander	5/23/2002
ICE-005PC	Frequency Offset Generator for Synthesized Signals	Expired	CA02/01499	PCT	Birkett, Alexander	10/4/02
ICE-005AU**	Frequency Offset Generator for Synthesized Signals	Lapsed	2002328745	Australia	Birkett, Alexander	10/4/2002
ICE-006	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	10/273,908	U.S.A.	Parker, Kevin	10/18/2002
ICE-006JP	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	2004-543858	Japan	Parker, Kevin	04/15/2005

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-006AU**	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Lapsed	2003278003	Australia	Parker, Kevin	10/14/2003
ICE-006PC	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Expired	2004036862	PCT	Parker, Kevin	4/29/2004
ICE-007	Systems and Modules for Use with Trellis-Based Decoding	Pending	10/377,859	U.S.A.	Amer, Maher	2/28/2003
ICE-007PC	Viterbi Decoder Operating In Units Of a Plurality Of Transitions	Expired	CA04/000282	PCT	Amer, Maher	2/26/04
ICE-008PR	Parallel Convolutional Encoder	Expired	60/399,728	U.S.A.	Amer, Maher	8/1/2002
ICE-008	Parallel Convolutional Encoder	Pending	10/629,644	U.S.A.	Amer, Maher	7/29/2003
ICE-008KR	Parallel Convolutional Encoder	Pending	7001719/2005	Korea	Amer, Maher	01/31/2005
ICE-008CN	Parallel Convolutional Encoder	Pending	03818236.X	China	Amer, Maher	07/31/2003
ICE-008JP	Parallel Convolutional Encoder	Pending	2004-525088	Japan	Amer, Maher	03/24/2005

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-008PC	Parallel Convolutional Encoder	Nationalized	CA03/0113	PCT	Amer, Maher	07/31/03
ICE-008AU**	Parallel Convolutional Encoder	Lapsed	2003249822	Australia	Amer, Maher	7/31/2003
ICE-009PR	Parallel Scrambler Descrambler	Expired	60/411,343	U.S.A.	Amer, Maher	9/18/02
ICE-009	Parallel Scrambler/Descrambler	Pending	10/629,640	U.S.A.	Amer, Maher	7/29/2003
ICE-009PC	Parallel Scrambler/Descrambler	Expired	CA03/01132	PCT	Amer, Maher	7/31/2003
ICE-009AU**	Parallel Scrambler/Descrambler	Lapsed	2003249821	Australia	Amer, Maher	7/31/2003
ICE-010PR	Processing Engines and RF Circuitry for Multi-Carrier Modulation Transceivers	Expired	60/277,941	U.S.A.	Wight, James	3/23/01

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-010	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	09/918,106	U.S.A.	Wight, James	7/30/2001
ICE-010PC	Signal Decomposition for The Control Of its Dynamic Range	Nationalized	CA02/001174	PCT	Wight, James	7/29/2002
ICE-010CA	Signal Decomposition for The Control Of its Dynamic Range	Abandoned but Revivable	2,455,277	Canada	Wight, James	7/29/2002
ICE-010CN	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Pending	20818664.8	China	Wight, James	7/29/2002
ICE-010EP	Signal Decomposition for The Control Of its Dynamic Range	Pending	2748528.3	EPO	Wight, James	7/29/2002
ICE-010JP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	2003-518144	Japan	Wight, James	7/29/2002
ICE-010KR	Computational Circuits and Methods for Processing Modulated Signals Having	Pending	7001445/2004	Korea	Wight, James	01/30/2004

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
	Non-Constant Envelopes					
ICE-010NO	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned but Revivable	20040367	Norway	Wight, James	1/27/2004
ICE-010CP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes (CIP)	Pending	10/205,743	U.S.A.	Wight, James	7/26/2002
ICE-011	Chireix Architecture Using Low Impedance Amplifiers	Issued	6836183	U.S.A.	Wight, James	10/16/2002
ICE-011JP	Chireix Architecture Using Low Impedance Amplifiers	Pending	2004-543859	Japan	Wight, James	04/15/2005
ICE-011PC	Chireix Architecture Using Low Impedance Amplifiers	Nationalized	CA03/001546	PCT	Wight, James	10/14/2003
ICE-011EP	Chireix Architecture Using Low Impedance Amplifiers	Pending	03769084	EPO	Wight, James	10/14/2003

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ICE-011AU**	Chireix Architecture Using Low Impedance Amplifiers	Lapsed	2003278004	Australia	Wight, James	10/14/2003
ICE-012	Memory Systems and Method for Use In Trellis-Based Decoding	Pending	10/377,860	U.S.A.	Amer, Maher	2/28/2003
ICE-013	Predistortion Circuit for a Transmit System	Pending	10/613,355	U.S.A.	Saed, Aryan	7/3/2003
ICE-013CP	Predistortion Circuit for a Transmit System (CIP)	Pending	10/641,370	U.S.A.	Saed, Aryan	8/13/2003
ICE-014	A Method Of and Device for Antennae Diversity Switching	Pending	10/610,454	U.S.A.	Saed, Aryan	6/30/2003
ICE-014PC	A Method Of and Device for Receive Antennae Diversity Switching	Pending	CA04/000949	PCT	Saed, Aryan	6/23/04
ICE-015	Adaptive Predistortion for a Transmit System	Allowed	10/613,372	U.S.A.	Saed, Aryan	7/3/2003
ICE-015CP	Adaptive Predistortion for a Transmit System (CIP)	Allowed	10/641,372	U.S.A.	Saed, Aryan	8/13/2003

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-016	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining	Issued	6,879,209	U.S.A.	Grundlingh, Johan	7/8/2003
ICE-016CP	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining (CIP)	Pending	11/099,916	U.S.A.	Grundlingh, Johan	4/6/2005
ICE-017	Integrated Circuit Incorporating Wire Bond Inductance	Pending	10/610,497	U.S.A.	Wight, James	6/30/2003
ICE-018	Digital Branch Calibrator for An RF Transmitter	Pending	10/627,881	U.S.A.	Saed, Aryan	7/25/2003
ICE-019	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Allowed	10/613,856	U.S.A.	Saed, Aryan	7/3/2003
ICE-019PC	Adaptive Predistortion for a Transmit System with Gain,	Pending	CA04/000972	PCT	Saed, Aryan	6/30/2004

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	Phase and Delay Adjustments					
ICE-019CP1	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,371	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP2	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,374	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP3	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,373	U.S.A.	Saed, Aryan	8/13/2003
ICE-020	Staggered AGC with Digitally Controlled VGA	Pending	10/661,945	U.S.A.	Birkett, Neil	9/12/2003
ICE-020PC	Staggered AGC with Digitally Controlled VGA	Pending	CA04/001566	PCT	Birkett, Neil	8/26/2004
ICE-021	Optimized FFT/IFFT Module	Pending	10/662,063	U.S.A.	Amer, Maher	9/12/2003
ICE-022	Method for Amplitude Insensitive Packet Detection	Pending	10/661,943	U.S.A.	Birkett, Neil	9/12/2003

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-022PC	Method for Amplitude Insensitive Packet Detection	Pending	CA04/001565	PCT	Birkett, Neil	8/26/2004
ICE-023	Frequency Domain Equalizer for Wireless Communications System	Pending	10/661,147	U.S.A.	Saed, Aryan	9/12/2003
ICE-023PC	Frequency Domain Equalizer for Wireless Communications System	Pending	CA04/001564	PCT	Saed, Aryan	8/26/04
ICE-029	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Pending	10/779,322	U.S.A.	Wight, James	2/13/2004
ICE-029PC	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Pending	CA05/000153	PCT	Wight, James	2/7/2005
ICE-030	Systems and Methods for Rapid Signal Detection and Identification	Pending	10/883,170	U.S.A.	Moher, Michael L.	07/01/2004
ICE-031	Multiple Input, Multiple Output Communications Systems	Pending	10/884,633	U.S.A.	Wight, James	07/02/2004

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-031C1	Multiple Input, Multiple Output Communications Systems (CIP)	Pending	10/954,429	U.S.A.	Wright, James	09/30/2004
ICE-032	Power Amplifier	Pending	10/884,627	U.S.A.	Parker, Kevin	7/02/2004
ICE-033PR	Improved Power Amplifier and Related Methods.	Expired	60/325,301	U.S.A.	Grundlingh, Johan	11/05/2004
ICE-033	Power Amplifier	Pending	Not yet assigned	U.S.A.	Grundlingh, Johan	11/07/2005

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Exhibit B

The terms and conditions of this Assignment of Patent Rights shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at _____ on _____

ASSIGNOR

By: Michael F. Schiavo

Name: Michael F. Schiavo

Title: Director

(Signature MUST be notarized)

STATE OF Massachusetts
COUNTY OF Worcester) ss.

On November 23, 2005, before me, Kristine Cunningham, Notary Public in and for said State, personally appeared Michael F. Schiavo, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.

Signature

Kristine Cunningham



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